6PIN POWER Header				
Pin	name	type	direction	description
1	U2_12V	Power	Input	Power supply for blade3
2	U2_12V	Power	Input	Power supply for blade3
3	U2_12V	Power	Input	Power supply for blade3
4	GND	Power	Input	Power reference ground.
5	GND	Power	Input	Power reference ground.
6	GND	Power	Input	Power reference ground.

SATA Connector				
Pin	name	type	direction	description
1	GND	Power	NA	Signal reference ground.
2	SATA_TXP	LVDS	Output	SATA30 Port0 transmit differential Positive
3	SATA_TXN	LVDS	Output	SATA30 Port0 transmit differential Negative
4	GND	Power	NA	Signal reference ground.
5	SATA_RXN	LVDS	Input	SATA30 Port0 receive differential Negative
6	SATA_RXP	LVDS	Input	SATA30 Port0 receive differential Positive
7	GND	Power	NA	Signal reference ground.

SFF-8643 Connector DM				
Pin	name	type	direction	description
A1	PCIE30_PORT0_REFCLKP	LVDS	Output	DM PCIe30 receive differential Positive
A2	PCIE30_PORT0_REFCLKN	LVDS	Output	DM PCIe30 receive differential Negative
A3	GND	Power	NA	Signal reference ground.
A4	PCIE30_PORT0_RX1P	LVDS	Input	DM PCIe30 receive differential Positive
A5	PCIE30_PORT0_RX1N	LVDS	Input	DM PCIe30 receive differential Negative
A6	GND	Power	NA	Signal reference ground.
A7	NC	Float	NA	No connected to this pin
A8	NC	Float	NA	No connected to this pin
A9	GND	Power	NA	Signal reference ground.
B1	PCIE30_PORT0_RESET	Signal	Input	DM PCIe30 Channel reset
B2	NC	Float	NA	No connected to this pin
B3	GND	Power	NA	Signal reference ground.
B4	PCIE30_PORT0_RX0P	LVDS	Input	DM PCIe30 receive differential Positive
B5	PCIE30_PORT0_RX0N	LVDS	Input	DM PCIe30 receive differential Negative
B6	GND	Power	NA	Signal reference ground.
B7	NC	Float	NA	No connected to this pin
B8	NC	Float	NA	No connected to this pin
B9	GND	Power	NA	Signal reference ground.
C1	NC	Float	NA	No connected to this pin
C2	NC	Float	NA	No connected to this pin
C3	GND	Power	NA	Signal reference ground.
C4	PCIE30_PORT0_TX1P	LVDS	Output	DM PCIe30 transmit differential Positive
C5	PCIE30_PORT0_TX1N	LVDS	Output	DM PCIe30 transmit differential Negative
C6	GND	Power	NA	Signal reference ground.
C7	NC	Float	NA	No connected to this pin
C8	NC	Float	NA	No connected to this pin
C9	GND	Power	NA	Signal reference ground.
D1	NC	Float	NA	No connected to this pin
D2	NC	Float	NA	No connected to this pin
D3	GND	Power	NA	Signal reference ground.
D4	PCIE30_PORT0_TX0P	LVDS	Output	DM PCIe30 transmit differential Positive
D5	PCIE30_PORT0_TX0N	LVDS	Output	DM PCIe30 transmit differential Negative
D6	GND	Power	NA	Signal reference ground.
D7	NC	Float	NA	No connected to this pin
D8	NC	Float	NA	No connected to this pin

Pin	name	type	direction	description
D9	GND	Power	NA	Signal reference ground.

SFF-8643 Connector RC				
Pin	name	type	direction	description
A1	PCIE30_PORT1_REFCLKP	LVDS	Input	RC PCIe30 receive differential Positive
A2	PCIE30_PORT1_REFCLKN	LVDS	Input	RC PCIe30 receive differential Negative
A3	GND	Power	NA	Signal reference ground.
A4	PCIE30_PORT1_RX1P	LVDS	Input	RC PCIe30 receive differential Positive
A5	PCIE30_PORT1_RX1N	LVDS	Input	RC PCIe30 receive differential Negative
A6	GND	Power	NA	Signal reference ground.
A7	NC	Float	NA	No connected to this pin
A8	NC	Float	NA	No connected to this pin
A9	GND	Power	NA	Signal reference ground.
B1	PCIE30_PORT1_RESET	Signal	Output	RC PCIe30 Channel reset
B2	NC	Float	NA	No connected to this pin
B3	GND	Power	NA	Signal reference ground.
B4	PCIE30_PORT1_RX0P	LVDS	Input	RC PCIe30 receive differential Positive
B5	PCIE30_PORT1_RX0N	LVDS	Input	RC PCIe30 receive differential Negative
B6	GND	Power	NA	Signal reference ground.
B7	NC	Float	NA	No connected to this pin
B8	NC	Float	NA	No connected to this pin
B9	GND	Power	NA	Signal reference ground.
C1	NC	Float	NA	No connected to this pin
C2	NC	Float	NA	No connected to this pin
C3	GND	Power	NA	Signal reference ground.
C4	PCIE30_PORT1_TX1P	LVDS	Output	RC PCIe30 transmit differential Positive
C5	PCIE30_PORT1_TX1N	LVDS	Output	RC PCIe30 transmit differential Negative
C6	GND	Power	NA	Signal reference ground.
C7	NC	Float	NA	No connected to this pin
C8	NC	Float	NA	No connected to this pin
C9	GND	Power	NA	Signal reference ground.
D1	NC	Float	NA	No connected to this pin
D2	NC	Float	NA	No connected to this pin
D3	GND	Power	NA	Signal reference ground.
D4	PCIE30_PORT1_TX0P	LVDS	Output	RC PCIe30 transmit differential Positive
D5	PCIE30_PORT1_TX0N	LVDS	Output	RC PCIe30 transmit differential Negative
D6	GND	Power	NA	Signal reference ground.
D7	NC	Float	NA	No connected to this pin
D8	NC	Float	NA	No connected to this pin
D9	GND	Power	NA	Signal reference ground.

SFF-8639 Connector				
Pin	name	type	direction	description
S1	GND	Power	NA	Signal reference ground.
S2	SATA_RXP	LVDS	Output	SATA30 Port0 receive differential Positive
S3	SATA_RXN	LVDS	Output	SATA30 Port0 receive differential Negative
S4	GND	Power	NA	Signal reference ground.
S5	SATA_TXN	LVDS	Input	SATA30 Port0 transmit differential Negative
S6	SATA_TXP	LVDS	Input	SATA30 Port0 transmit differential Positive
S7	GND	Power	NA	Signal reference ground.
E1	PCIE30_PORT1_REFCLKP	LVDS	Input	RC PCIe30 receive differential Positive
E2	PCIE30_PORT1_REFCLKN	LVDS	Input	RC PCIe30 receive differential Negative
E3	NC	Float	NA	No connected to this pin
E4	NC	Float	NA	No connected to this pin
E5	PCIE30_PORT0_RESET	Signal	Output	DM PCIe30 Channel reset
E6	NC	Float	NA	No connected to this pin

P1	NC	Float	NA	No connected to this pin
P2	NC	Float	NA	No connected to this pin
P3	NC	Float	NA	No connected to this pin
P4	NC	Float	NA	No connected to this pin
P5	GND	Power	NA	Power reference ground.
P6	GND	Power	NA	Power reference ground.
P7	NC	Float	NA	No connected to this pin
P8	NC	Float	NA	No connected to this pin
P9	NC	Float	NA	No connected to this pin
P10	PCIE30_PORT1_RESET	Signal	Input	RC PCIe30 Channel reset
P11	NC	Float	NA	No connected to this pin
P12	GND	Power	NA	Power reference ground.
P13	U2_12V	Power	Input	Power supply for blade3
P14	U2_12V	Power	Input	Power supply for blade3
P15	U2_12V	Power	Input	Power supply for blade3
E25	NC	Float	NA	No connected to this pin
E24	NC	Float	NA	No connected to this pin
E23	NC	Float	NA	No connected to this pin
E22	GND	Power	NA	Signal reference ground.
E21	PCIE30_PORT1_TX1N	LVDS	Input	RC PCIe30 transmit differential Negative
E20	PCIE30_PORT1_TX1P	LVDS	Input	RC PCIe30 transmit differential Positive
E19	GND	Power	NA	Signal reference ground.
E18	PCIE30_PORT1_RX1N	LVDS	Output	RC PCIe30 receive differential Negative
E17	PCIE30_PORT1_RX1P	LVDS	Output	RC PCIe30 receive differential Positive
S28	GND	Power	NA	Signal reference ground.
S27	PCIE30_PORT1_TX0N	LVDS	Input	RC PCIe30 transmit differential Negative
S26	PCIE30_PORT1_TX0P	LVDS	Input	RC PCIe30 transmit differential Positive
S25	GND	Power	NA	Signal reference ground.
S24	PCIE30_PORT1_RX0N	LVDS	Output	RC PCIe30 receive differential Negative
S23	PCIE30_PORT1_RX0P	LVDS	Output	RC PCIe30 receive differential Positive
S22	GND	Power	NA	Signal reference ground.
S21	PCIE30_PORT0_RX1N	LVDS	Output	DM PCIe30 receive differential Negative
S20	PCIE30_PORT0_RX1P	LVDS	Output	DM PCIe30 receive differential Positive
S19	GND	Power	NA	Signal reference ground.
S18	PCIE30_PORT0_TX1N	LVDS	Input	DM PCIe30 transmit differential Negative
S17	PCIE30_PORT0_TX1P	LVDS	Input	DM PCIe30 transmit differential Positive
S16	GND	Power	NA	Signal reference ground.
S15	NC	Float	NA	No connected to this pin
S14	GND	Power	NA	Signal reference ground.
S13	NC	Float	NA	No connected to this pin
S12	NC	Float	NA	No connected to this pin
S11	GND	Power	NA	Signal reference ground.
S10	NC	Float	NA	No connected to this pin
S9	NC	Float	NA	No connected to this pin
S8	GND	Power	NA	Signal reference ground.
E16	NC	Float	NA	No connected to this pin
E15	GND	Power	NA	Signal reference ground.
E14	PCIE30_PORT0_RX0N	LVDS	Output	DM PCIe30 receive differential Negative
E13	PCIE30_PORT0_RX0P	LVDS	Output	DM PCIe30 receive differential Positive
E12	GND	Power	NA	Signal reference ground.
E11	PCIE30_PORT0_TX0N	LVDS	Input	DM PCIe30 transmit differential Negative
E10	PCIE30_PORT0_TX0P	LVDS	Input	DM PCIe30 transmit differential Positive
E9	GND	Power	NA	Signal reference ground.
E8	PCIE30_PORT0_REFCLKN	LVDS	Output	DM PCIe30 receive differential Negative
E7	PCIE30_PORT0_REFCLKP	LVDS	Output	DM PCIe30 receive differential Positive