



Product Change Notification / RMES-14NRSU939

Date:

19-Apr-2023

Product Category:

AntiFuse FPGAs, General Purpose FPGAs, Radiation Tolerant FPGAs, System On Chip FPGAs

PCN Type:

Manufacturing Change

Notification Subject:

eSign # E000169897 Final Notice: Release of updated Synplify Pro ME version S-2021.09M-SP2 synthesis software bundled with Libero SoC v2022.3 for various FPGA devices.

Affected CPNs:

[RMES-14NRSU939_Affected_CPN_04192023.pdf](#)
[RMES-14NRSU939_Affected_CPN_04192023.csv](#)

Notification Text:

PCN Status:Final Notification

PCN Type:Manufacturing Change

Microchip Parts Affected:Please open one of the files found in the Affected CPNs section.
Note: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change:Release of updated Synplify Pro ME version S-2021.09M-SP2 synthesis software bundled with Libero SoC v2022.3 as described in the attached customer notification details.

Note:

Libero SoC v2022.3 was released on December 16, 2022, and is available for download on the webpage below.

<https://www.microchip.com/en-us/products/fpgas-and-plds/fpga-and-soc-design-tools/fpga/libero-software-later-versions#Download%20Software>

Pre and Post Change Summary:

	Pre Change	Post Change
Software / Tool	All Synplify Pro releases prior to Synplify Pro ME version S-2021.09M-SP2 and Libero SoC v2022.2 or earlier versions	Synplify Pro ME release S-2021.09M-SP2 and later, bundled with Libero SoC v2022.3 and later. The issue has also been fixed in Synplify Pro standalone release T-2022.09 and later.

Impacts to Data Sheet:None

Change Impact:None

Reason for Change:Release updated Synplify Pro ME version S-2021.09M-SP2 synthesis software bundled with Libero SoC v2022.3 to prevent incorrect VHDL expression evaluation during compilation, when performing subtraction with a real constant number operand, and an additional division/multiplication operation that uses a variable. This is to fix a VHDL compiler issue that occurs in specific VHDL expressions.

Change Implementation Status:Complete

Estimated First Ship Date:December 16, 2022 (date code: 2251)

Note: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Time Table Summary:

	December 2022					>	April 2023				
Workweek	4 9	5 0	5 1	5 2	5 3		1 4	1 5	1 6	1 7	1 8
Final PCN Issue Date									x		
Implementation Date			x								

Method to Identify Change:Not applicable. New software is available as defined above.

Qualification Report:Not Applicable

Revision History:

April 19, 2023: Issued final notification to reveal the appropriate software release.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachments:

[CN_SynPro_FPGA_VHDL_Compiler_Libero_2022p3.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

5962-0054301QXC
5962-0054301QYC
5962-0054302QXC
5962-0054302QYC
5962-0120303QUF
5962-0150805QXC
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U1AFS250-FG256I
U1AFS250-FGG256
U1AFS250-FGG256I
U1AFS600-FG256
U1AFS600-FG256I
U1AFS600-FGG256
U1AFS600-FGG256I